

 *Barrys Scientific Based Products*



Barry's 1152 Bit CPU

By

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Introduction

I have created a new CPU Design that utilizes dual ring Network Typology's creating four Internal and eight external areas of space per ring coupled with node points to create linear paths and circular motion for new encryption methods and or techniques. This design works with IPV6 only. The design is upgraded and optimized using the previous 904 Bit CPU allowing the usage of Tunnelling protocols wires that are .20 cm thick with thin copper wire inside on all 9 wires. Inside the CPU it has nine spaces

My design will be geared toward the title "Barry's 1152 Bit CPU" Processing and utilizing 1152 Bits CPU technology. I have also included in this design a memory chip that can work with this CPU utilizing 9 characters per address to call programs from memory also using Dual network Topology Designs 12 areas of space also included is 4 Internal and 8 external node points on each Network Ring. The Network Topology in this design has a shell within a shell that is also Octagon in Nature.

I have updated the wiring configuration that is evenly distributed across 9 wires .20 cm thick and .10 thin wire to produce 128 bits per wire and usage of tunnelling protocols. The New CPU Design deploys 1 CPU with 9 octagon polygons built on to one CPU, the internal shell is within the shell and has 12 node points for the data to travel the best path available.

The Dual Ring Network Topology has been further developed to included a new file and hashing scheme complete with a new equation utilizing 13824 bits for file and hashing scheme. This file and hashing scheme deploys linear, circular, clockwise and counter clockwise motion.

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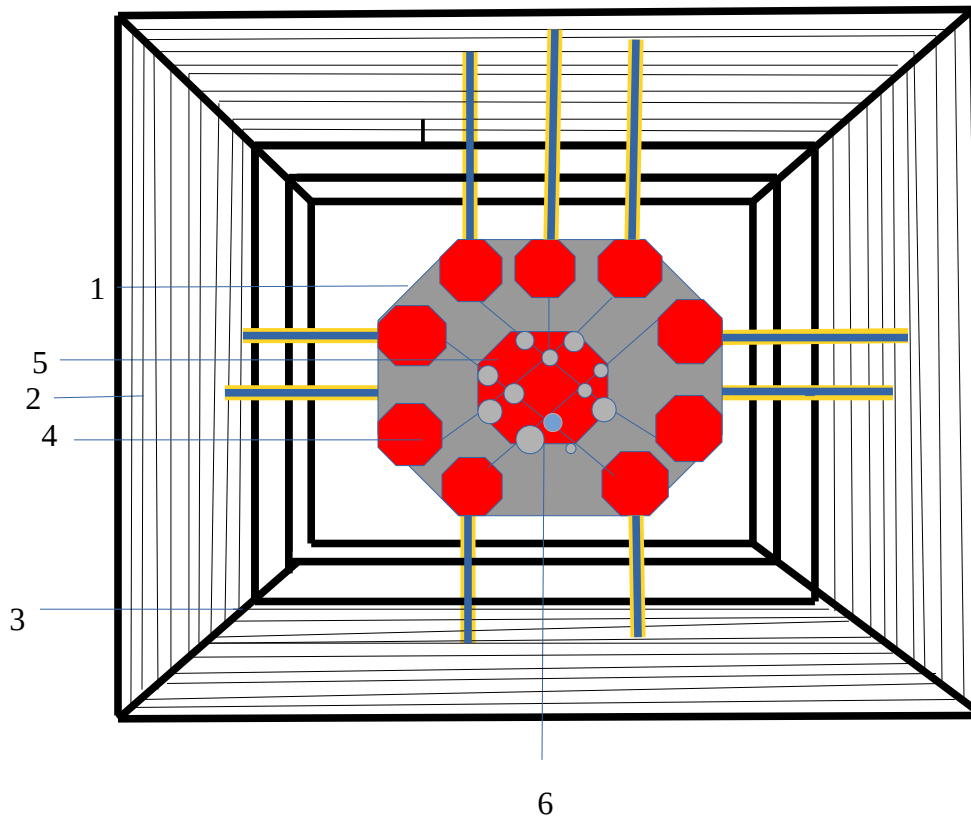
- 1). Chapter 1 Visual Design
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Chapter 1

Visual Design

1152 BIT CPU Visual Chart 1-A

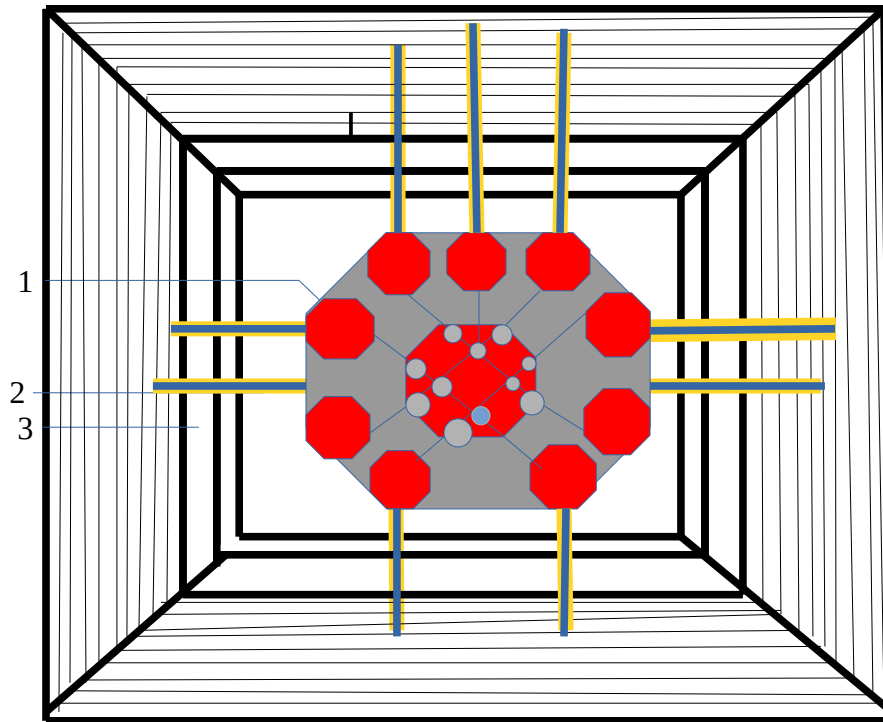
Inside Front View



- 1). CPU 1 (1152) Bits
- 2). Wires 1- 9 128 bits per wire .20 cm thick and thin wire .10 cm
- 3). Case Fitting
- 4.) 9 External Spaces
- 5). 1 Internal Topology Shell
- 6). 12 node points

1152 BIT CPU Visual Chart 2-A

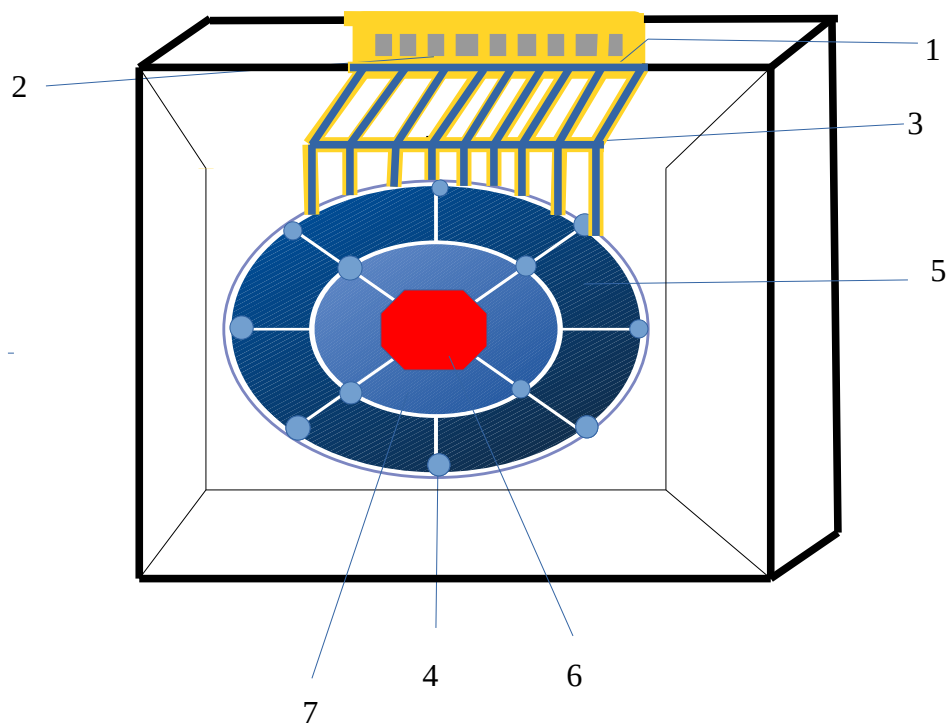
General View



- 1). CPU 1 (1152) Bits 9 External spaces, 1 Internal Topology shell, 12 Gateways
- 2). Wires 1- 9 128 bits per wire .20 cm thick copper .10 cm thin used for inside tunnelling protocol
- 3). Case Fitting

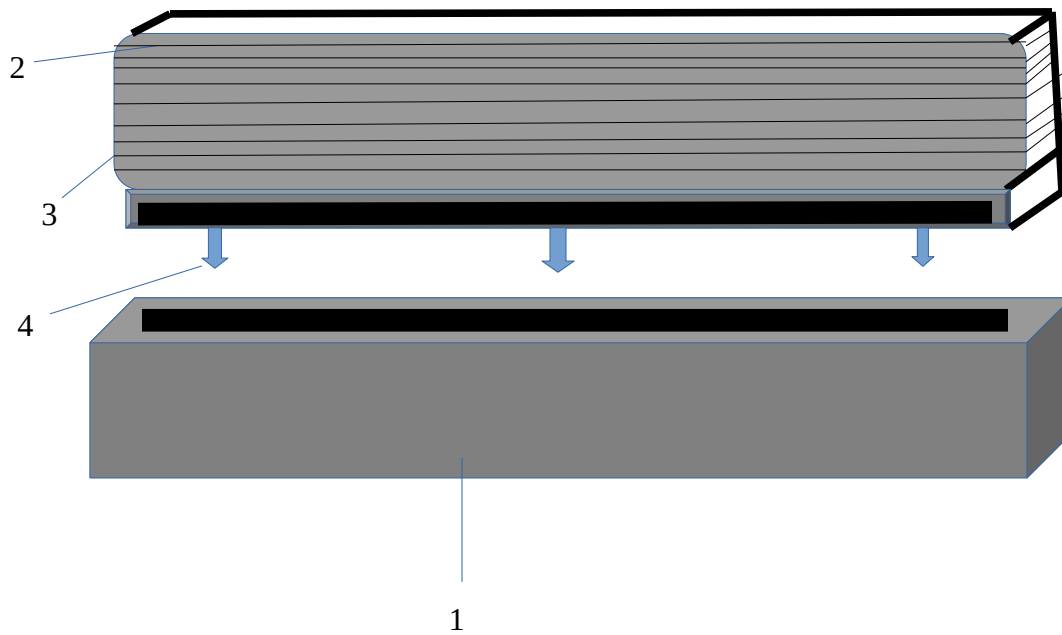
1152 BIT CPU Octagon Visual Chart 3-A

Wiring Block and Inside Ring Network



- 1). Terminator Block
- 2). 9 wires thick .20 cm = 128 bits per wire thin wire .10 cm total 1152 bits
- 3). Terminator Block
- 4). 12 Node Points
- 5). Ring Network Topology 2 = 8 Nodes
- 6). Internal Shell
- 7). Ring Network Topology 1 = 4 nodes

Memory Chips that work with 1152 Bit CPU visual Chart 4-a



- 1). Slot for Memory Chip
- 2). Double sided memory slot 1 side = 192 bits total 1152 bits maximum 3 memory slots $384 * 3 = 1152$
- 3). 9 Wires inside memory chip
- 4). Memory chip rests on memory slot.
- 5). 9 Characters = 1 memory address to call programs from the CPU .

Chapter 2

Specifications

I will now go over the specifications. The Shape of the CPU Design uses multiple interior Octagons that uses a 8 sided Polygon to create Nine External spaces and 1 Internal Network shell with 12 node points for a total of 1152 Bits with thick wires that utilize the following .20 cm thick and interior thin wire .10 cm =128 bits on the back of the design see chart 3-a. The front uses .20 cm thick with thin copper wire for usage of Tunnelling Protocols using IPV6 only.

The CPU has a built in Network Typology's to create 12 areas of space also created on the rings are Node Points that uses linear paths and accesses concentric circles built on the CPU that has access to it. The concentric circles and linear paths can now create encryption shielding CPU's from BIOS based attacks instead of broadcasting Bios based software. The total node points are 8 external and 4 internal for a total of 12. From within this Internal shell, is another Internal shell The point within the Internal Shell allows it to access the built in Network Topology this demonstrates the principle of Dimensional space from the CPU to the Network Topology no point of origin if you notice.

The Ring Network is configured for the CPU . The back of the CPU has Bridge two terminator blocks on the bridge. Please also note the Network Topology utilizes linear and circular motion in processing data to the CPU's.

As a added feature, I have added a new memory chip design that is compatible to this new CPU design deploying 9 character address fields making the address scheme to the CPU to be unique for calling programs from the CPU.

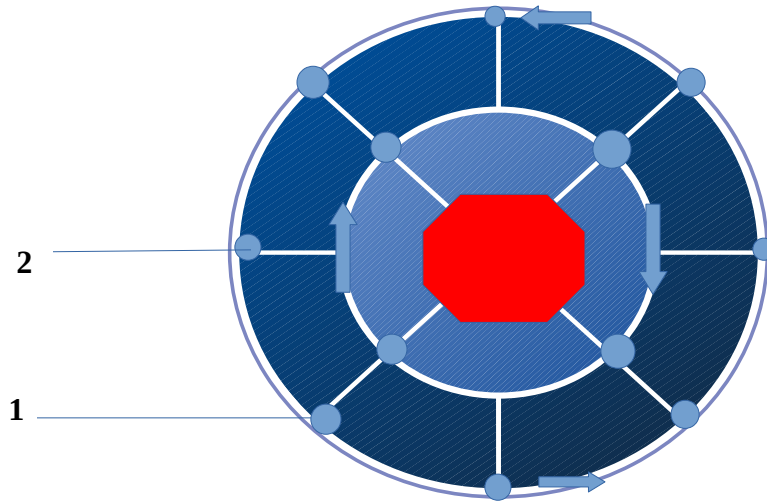
After the bridge, The final switch that goes to the wires processes 1152 bits on the Network Ring to create encryption for the CPU's making a total of 1152 bits see visual aide 1-A CPU.

I have created 12 area's of space for the CPU to choose the best path available for data to travel by utilizing the node points within the shell. see overall view 3-a item number 2 and 4.

Chapter 3

CPU 1152 and 13824 File Hashing and Algorithm

CPU Ring Network Topology and Data Encryption 5-A



I can now run a Encryption program that uses double ring network topology clockwise and counter clockwise motion creating a new equation or process. The following variables are defined:

- 1). a = Internal shell 4 node points
- b). b = External shell 8 node points

The number or constant utilized 13824

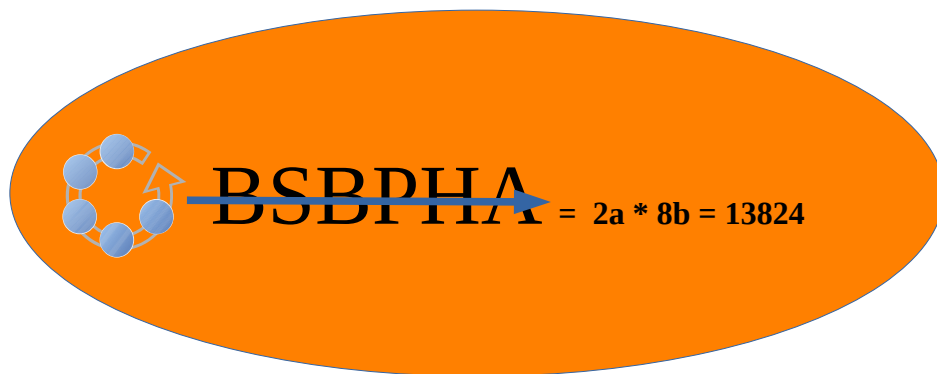
- 1). CPU 1152 = $2a * 8b = 13824$
- 2). CPU 1152 = $(2*4) * (8*1) = 13824$
- 3). CPU 1152 = $8 * 8 = 64$
- 4). CPU 1152 = $13824 / 64 = 216$

I have in the past used 8192 and 12288 as a basis for various Operations but with the new file and hashing scheme using 13824 rather than the 12288 this sets it apart as a novel idea and is very patentable.

You may ask what does the 13824 file hashing block program do for me ? If you look at this a little closer , We find that I have a data block of 13824 not the standard 2048 or 4096 block of data. I then create a hashing scheme of 13824 / 64 utilizing the double ring Network topology and equation just presented and create a hashing scheme of 216 thus each 13824 block of data can be hashed for 216 with a total of 216 entries per 64. My files within a index can have up to 216 per block and as stated before I can represent each address with 8 character fields I can add and or mask the 8 character field and pad it with 8 additional characters for masking thus $8*8 = 64$ for additional security by masking the address commands. The file and hashing method in chart 5-a uses clockwise and counter clockwise motion. The equation is very simple but deploys the usage of node networks adhering to Energy being dynamic in nature. I will now present the new File Hashing Equation with some symbols.



$$= a \quad b =$$



Final Thoughts

Chapter 4

1). I have completed a new 1152 bit CPU Design. This design uses node points, Gateways, Internal shells, string paths using linear and circular motion and is upgraded from 904 to 1152 bits

2). The new CPU design is scaled for 1152 bits using IPV6.

3). I have also created a new Memory chip that is compatible with this new CPU Design that is double sided using 192 bits per one side for a total of 384 bits per stick for a total of 3 memory slots that can be used on a motherboard.

4). The memory chip processes 9 character address fields used to call programs from the CPU also the memory chip has 9 wires inside upgraded from 8 to 9. The 9 character field used for commands can be padded or masked to protect Internal address fields used by CPU's to call programs from memory.

5). The network topology built inside the CPU has a added feature of a shell within a shell also wiring is now .20 cm thick thin wire .10 cm to allow tunnelling protocols to be configured through the wire also this new encryption equation is further developed using this new Dual Ring Network Topology allowing for 216 file index hashing using 13824 blocks of data.

6). There are two different areas that should be remembered that is the payload data using IPV6 and the file and hashing scheme one uses 9 character fields the other uses 8 character field. When loading data to be sent out to the internet the file of $13824 / 9 =$ mtu size of 1536 works with the file and hashing scheme size.

7). A new Equation has been written to accompany this work and looks like very patentable material new design or process.

I would like thank each and everyone of you for viewing this work because it is worth considering.

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